

Docket No. 100201346
NANOMETER SCALE SEMICONDUCTOR
DEVICES AND METHOD OF MAKING
James Stasiak, et al.

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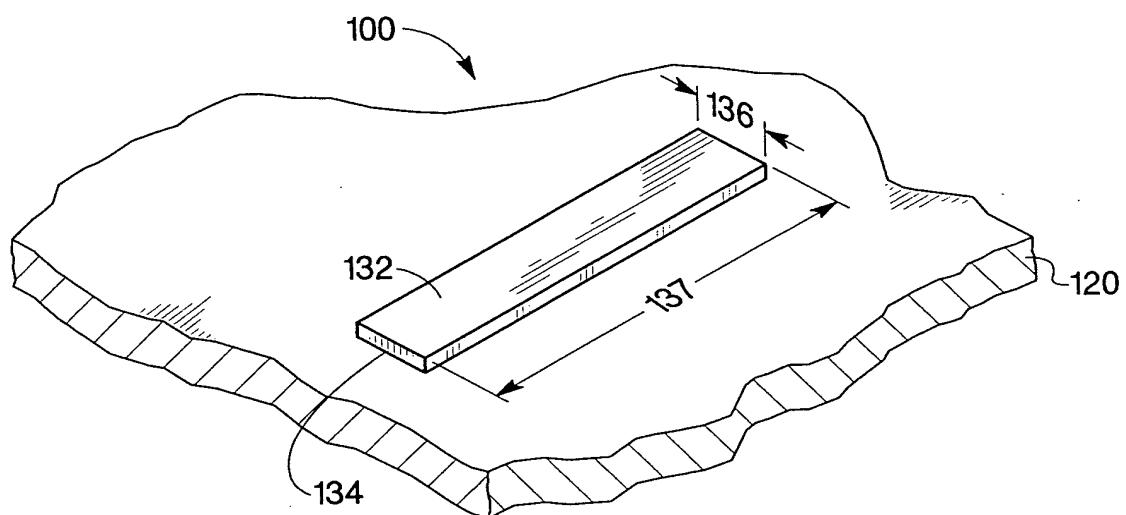


Fig. 1

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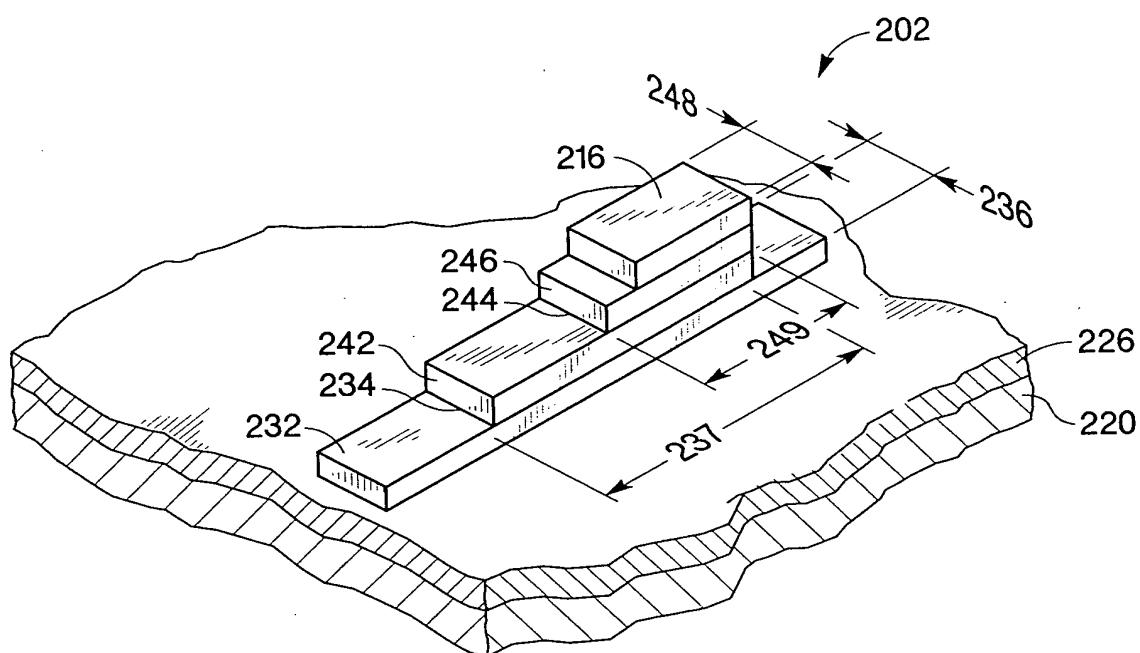


Fig. 2a

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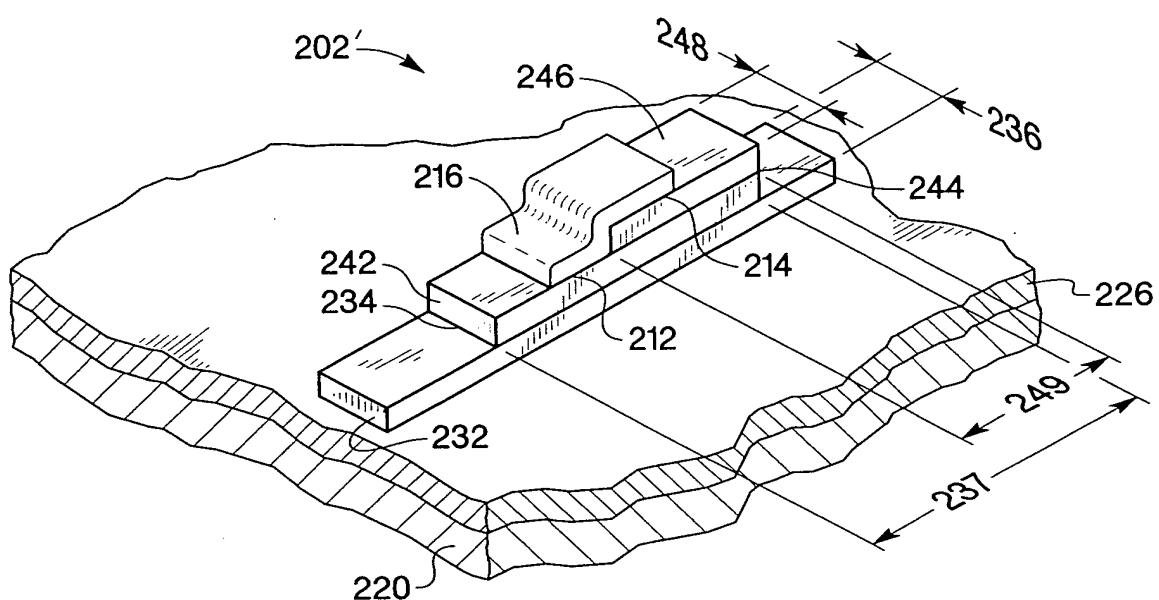


Fig. 2b

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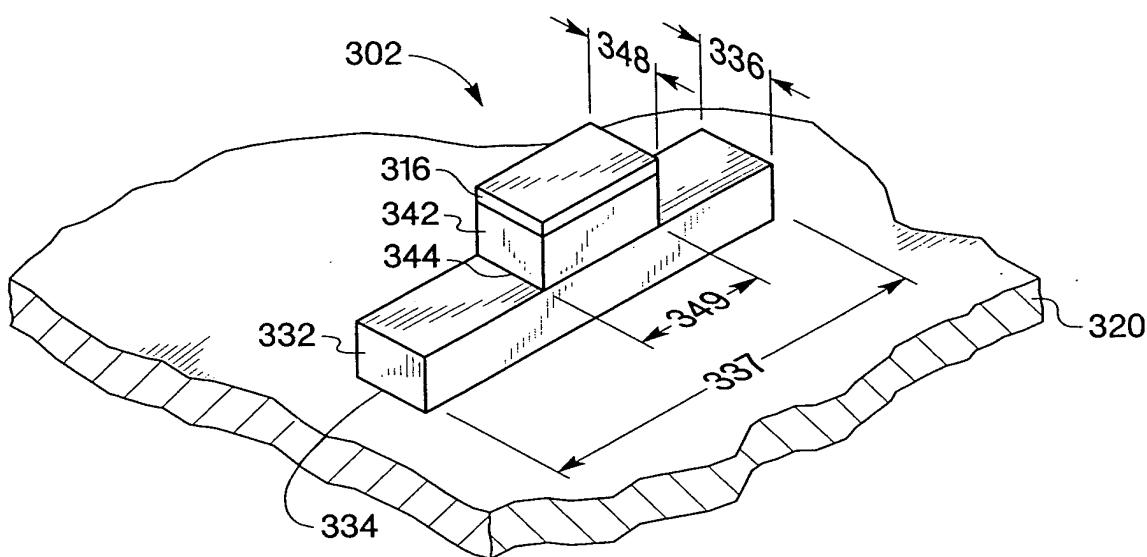


Fig. 3

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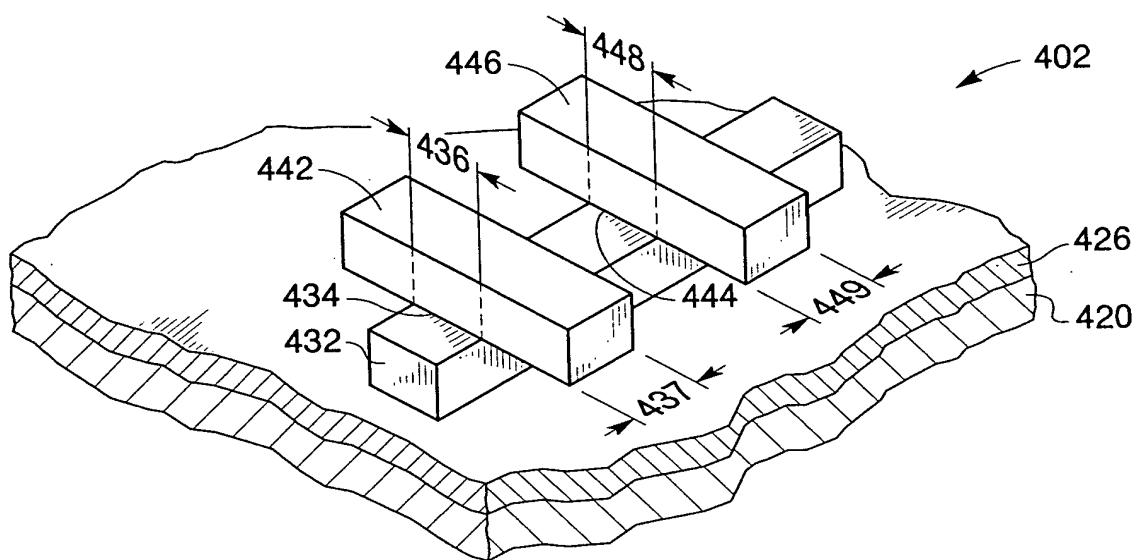


Fig. 4a

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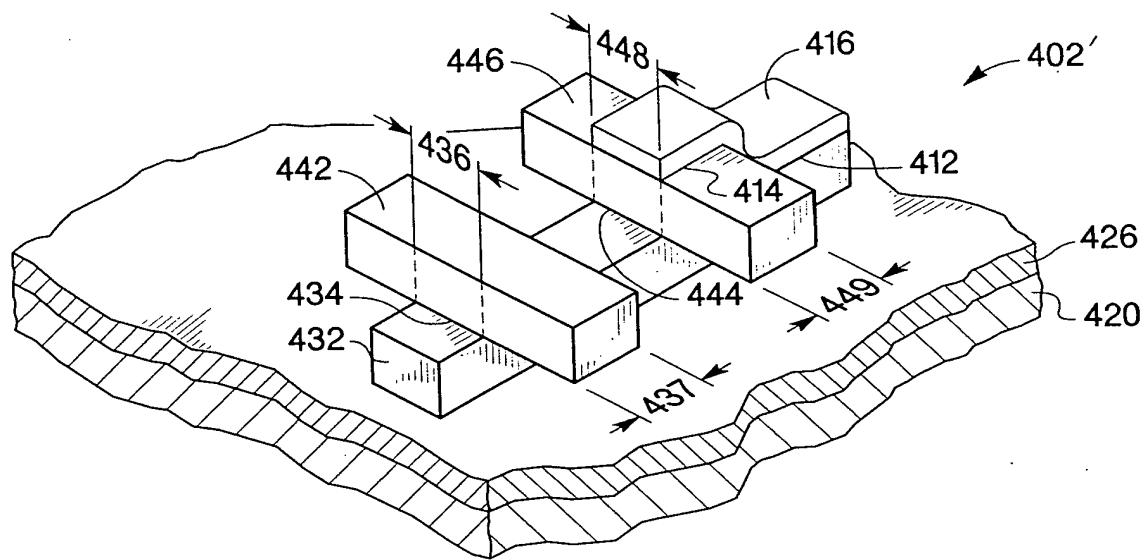


Fig. 4b

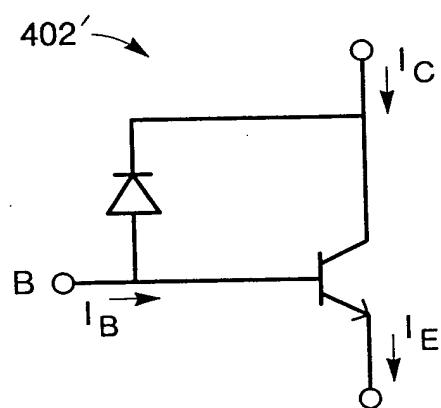


Fig. 4c

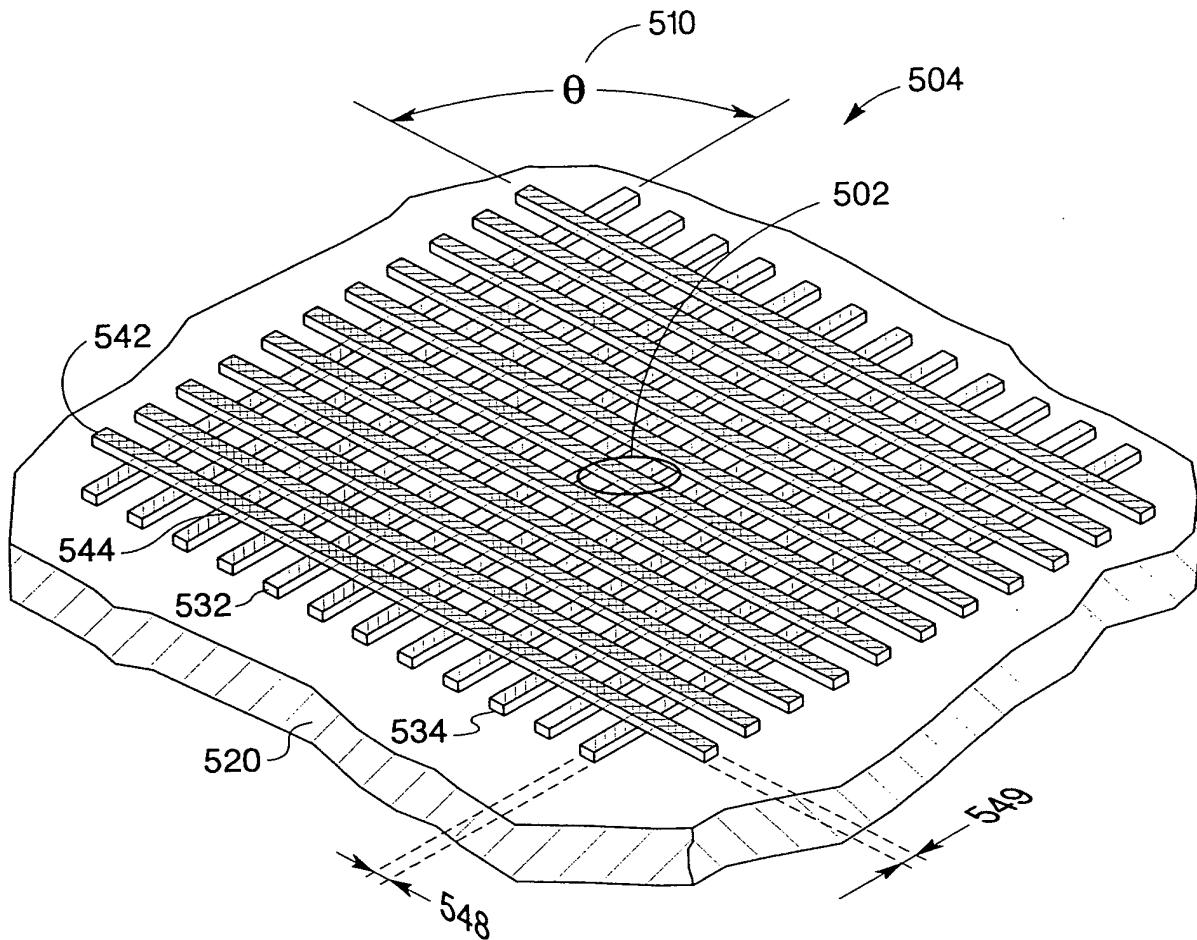


Fig. 5a

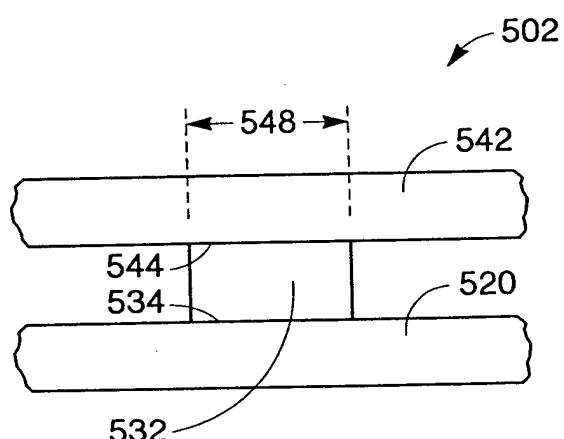


Fig. 5b

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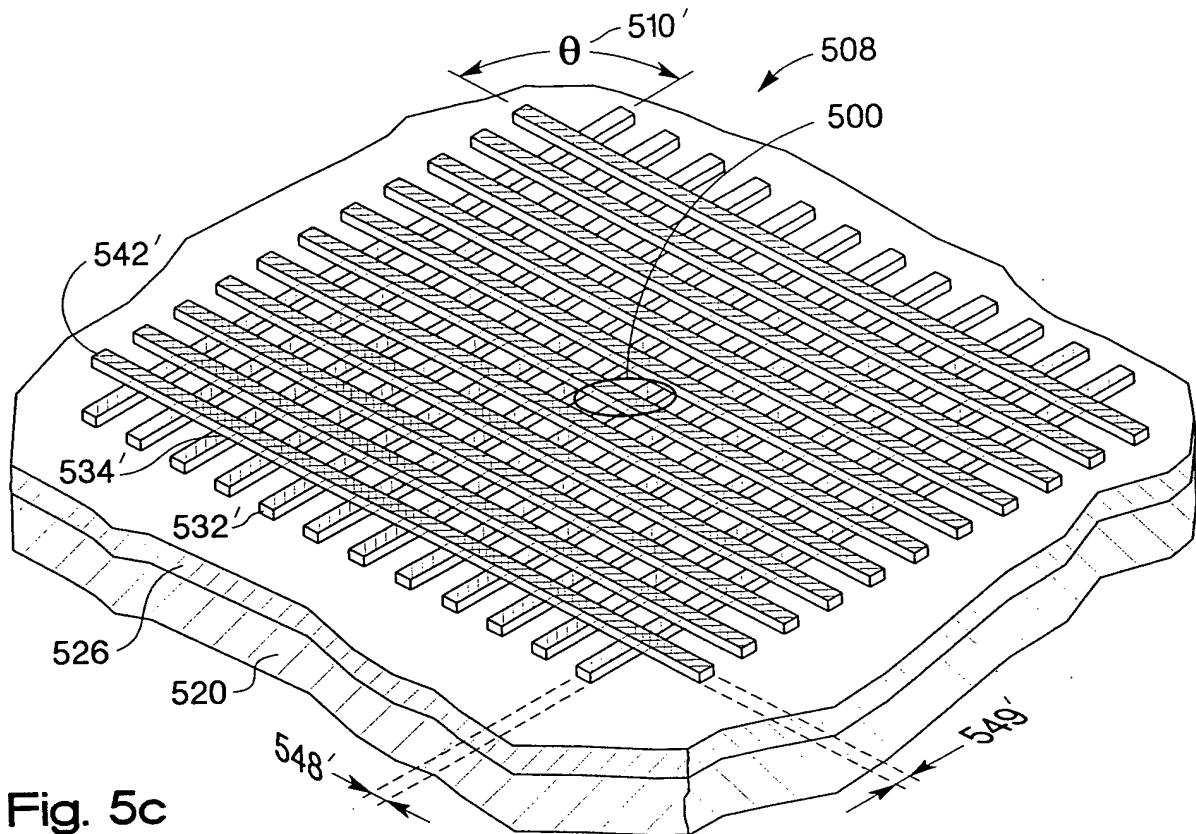


Fig. 5c

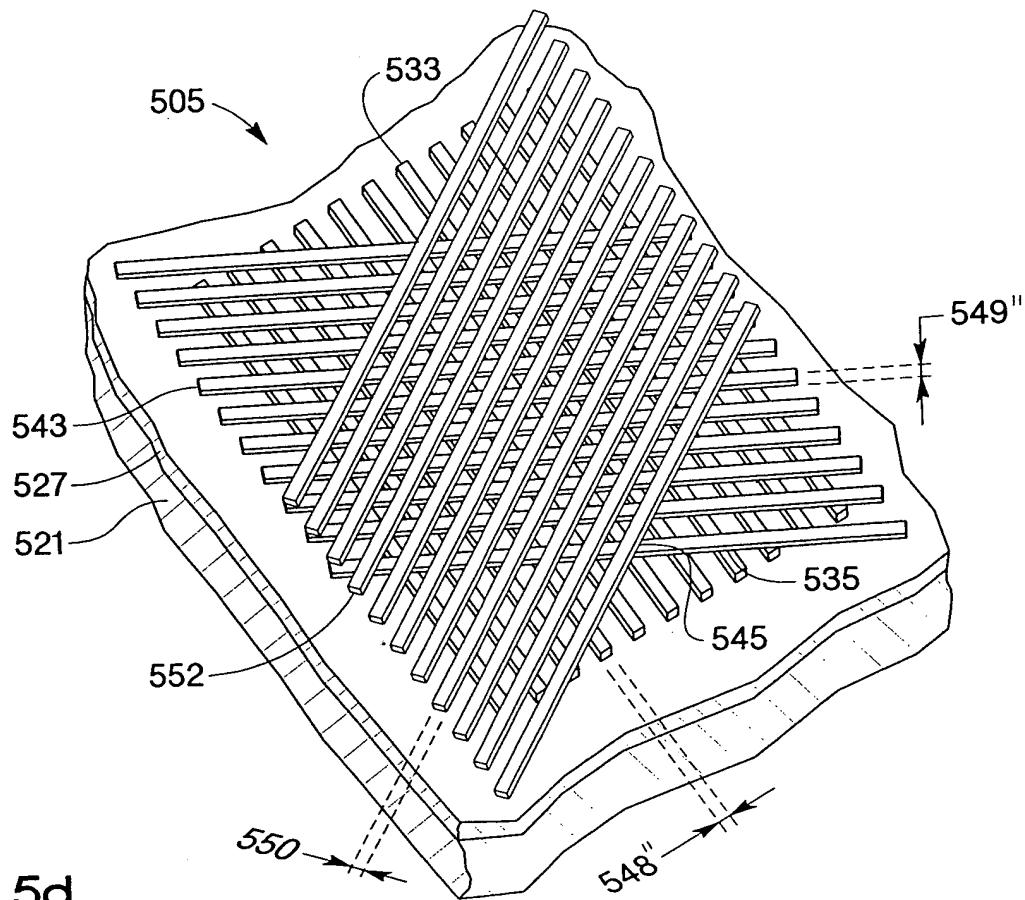


Fig. 5d

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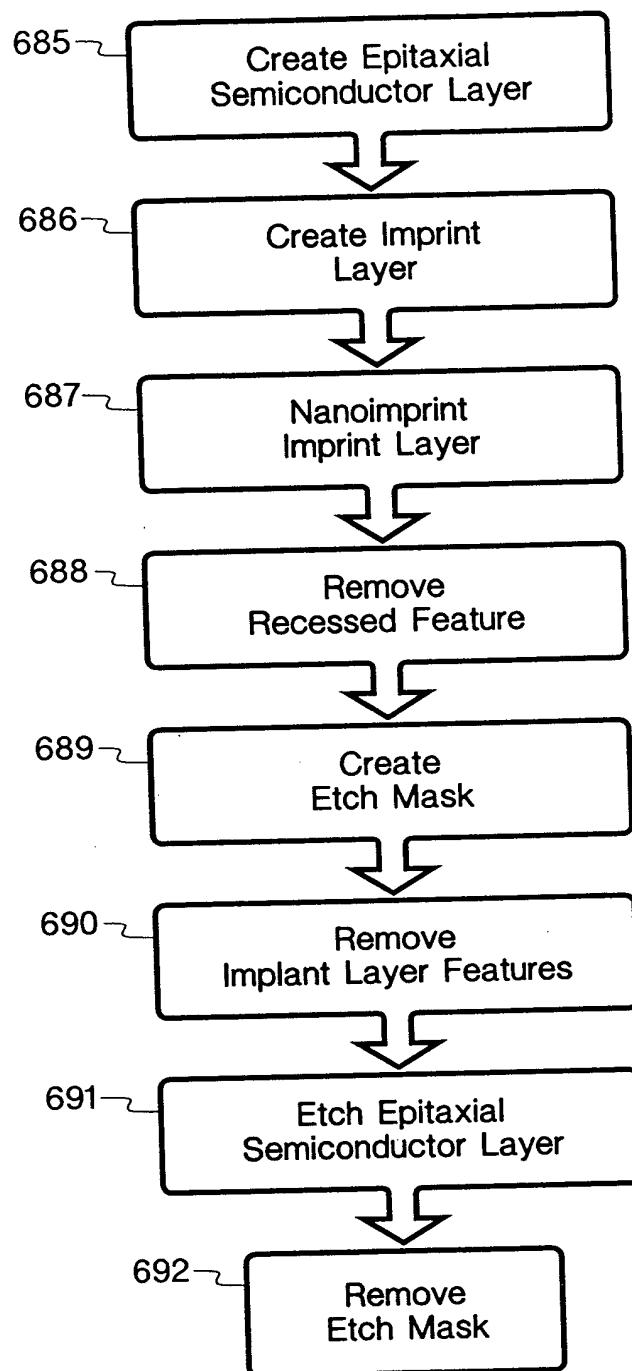


Fig. 6

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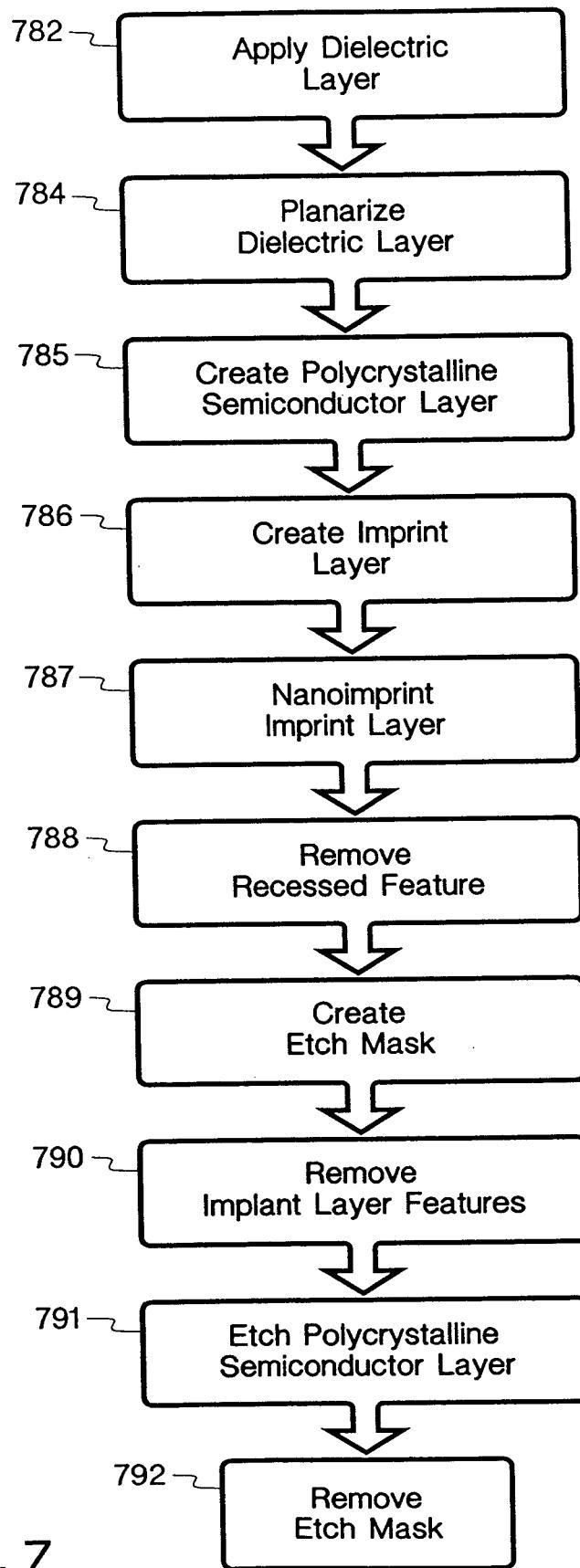


Fig. 7

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Fig. 8a

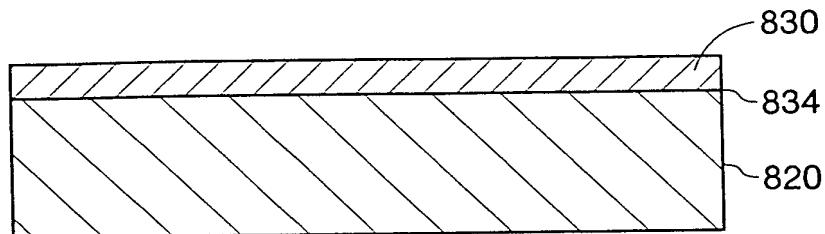


Fig. 8b

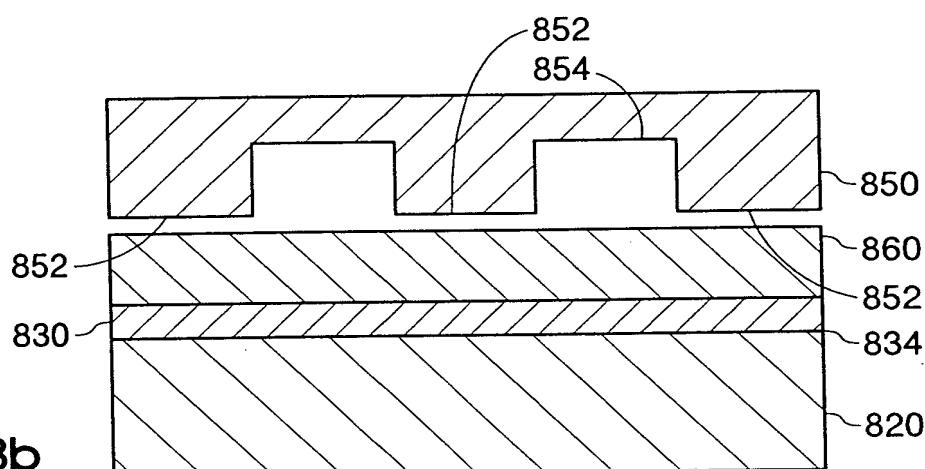


Fig. 8c

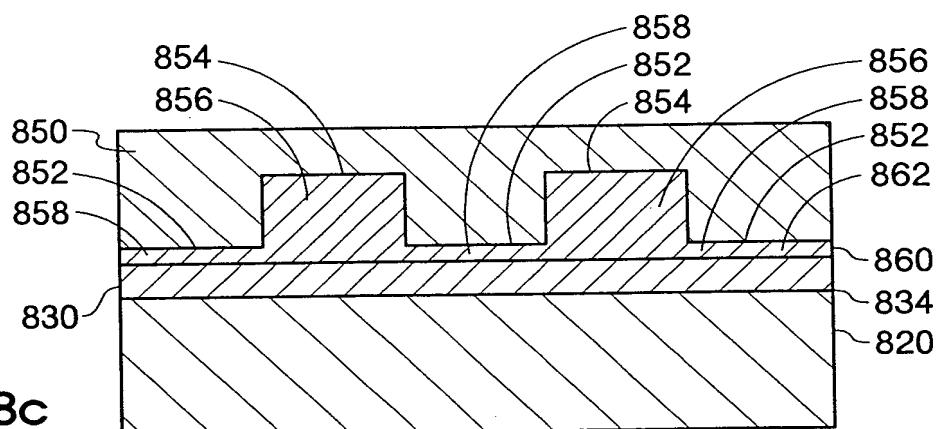
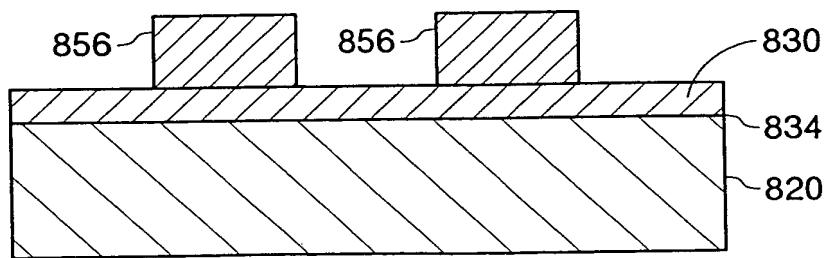


Fig. 8d



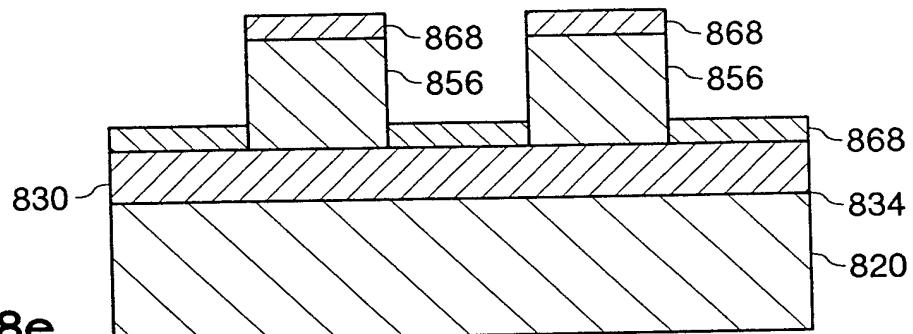


Fig. 8e

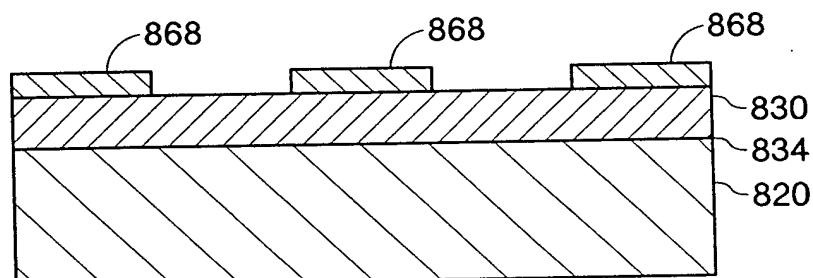


Fig. 8f

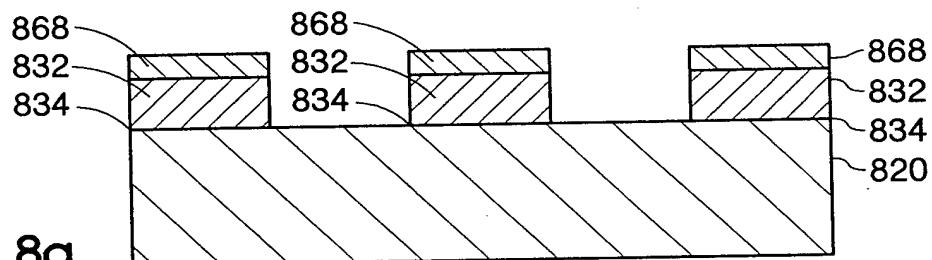


Fig. 8g

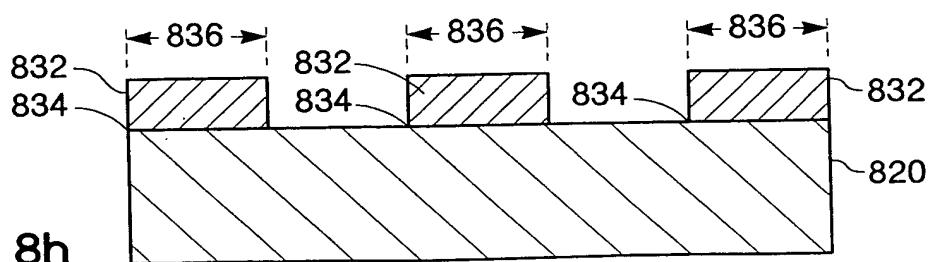


Fig. 8h

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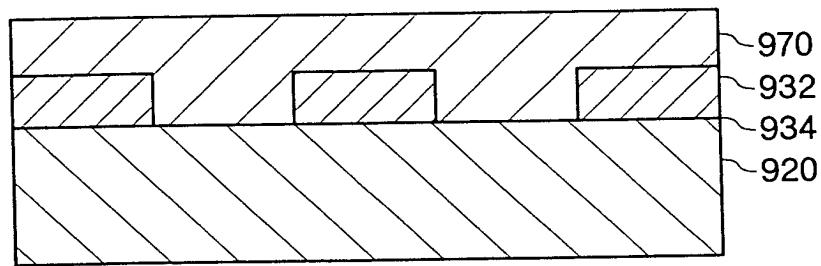


Fig. 9a

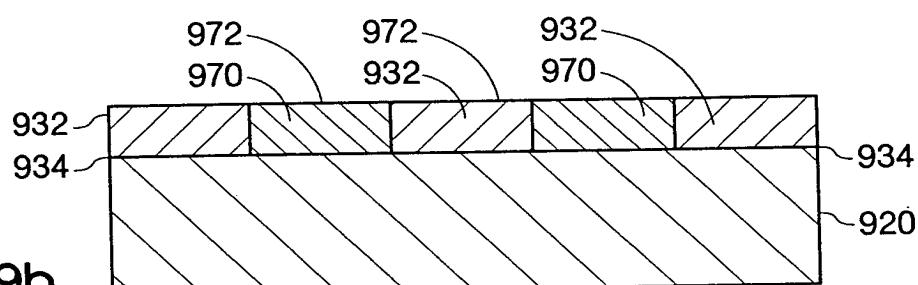


Fig. 9b

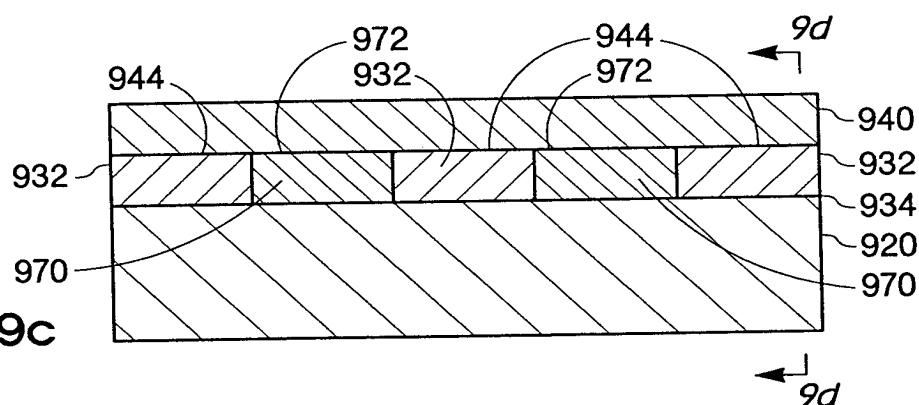


Fig. 9c

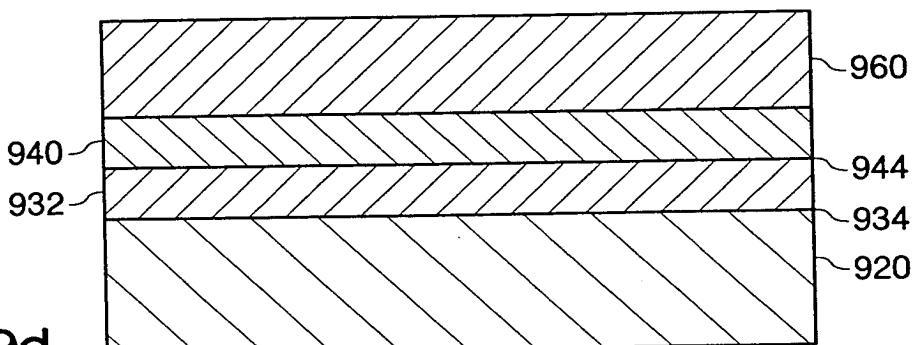


Fig. 9d

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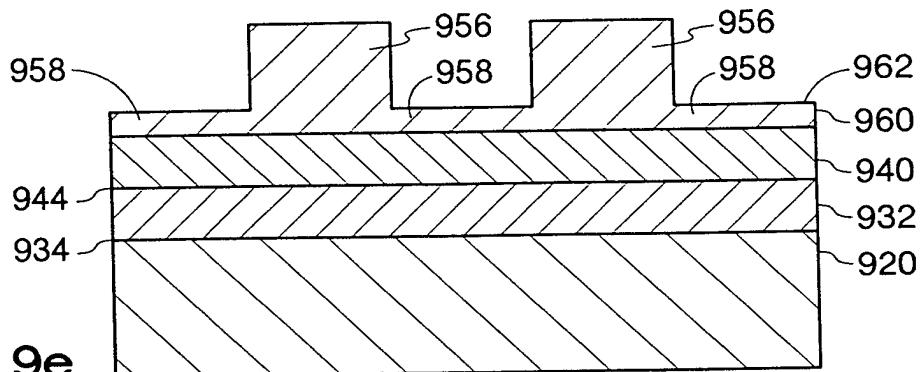


Fig. 9e

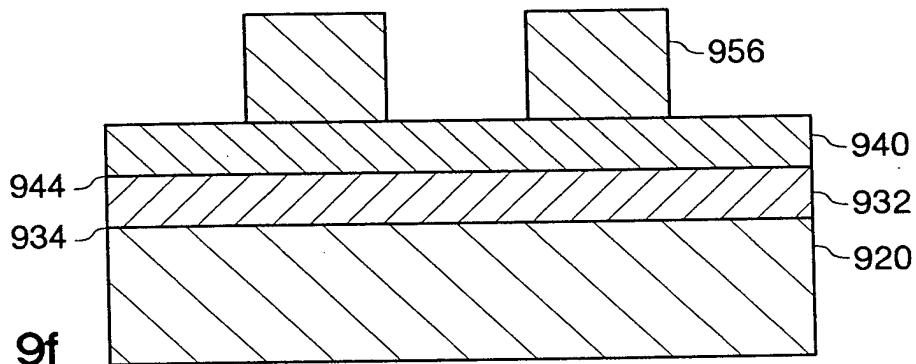


Fig. 9f

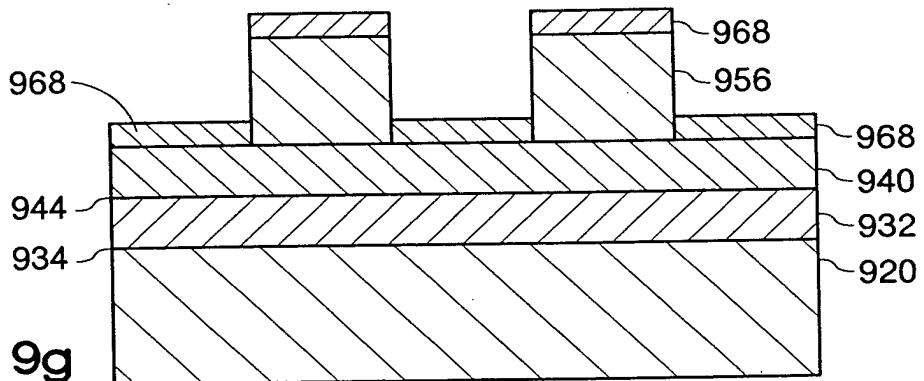


Fig. 9g

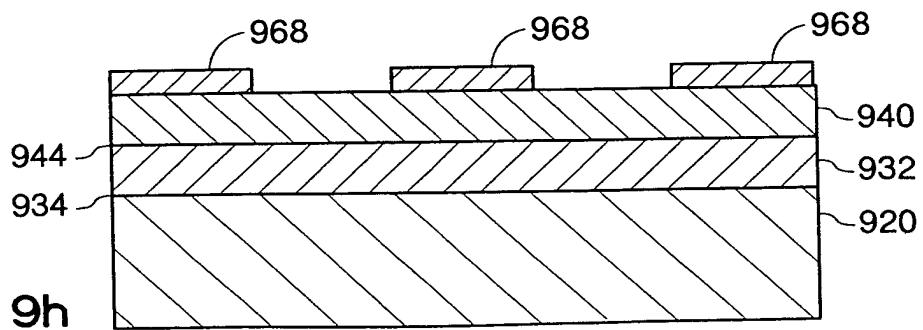


Fig. 9h

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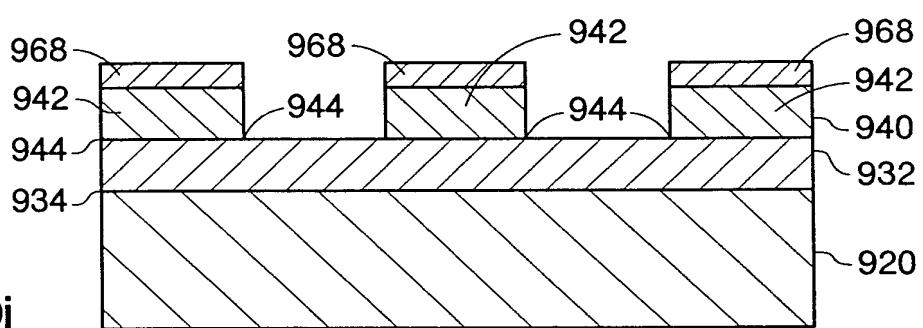


Fig. 9i

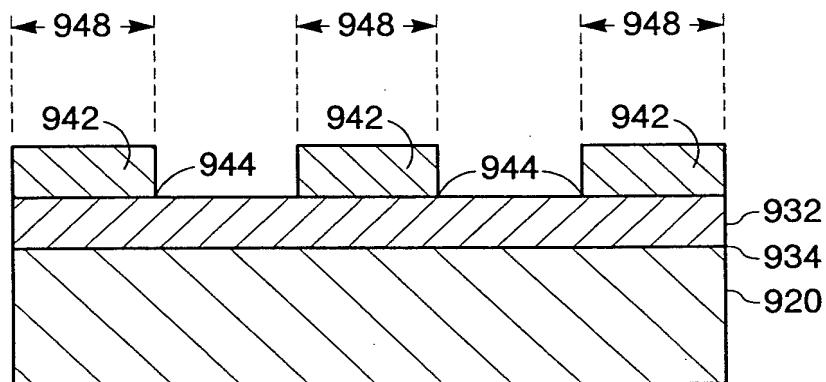


Fig. 9j

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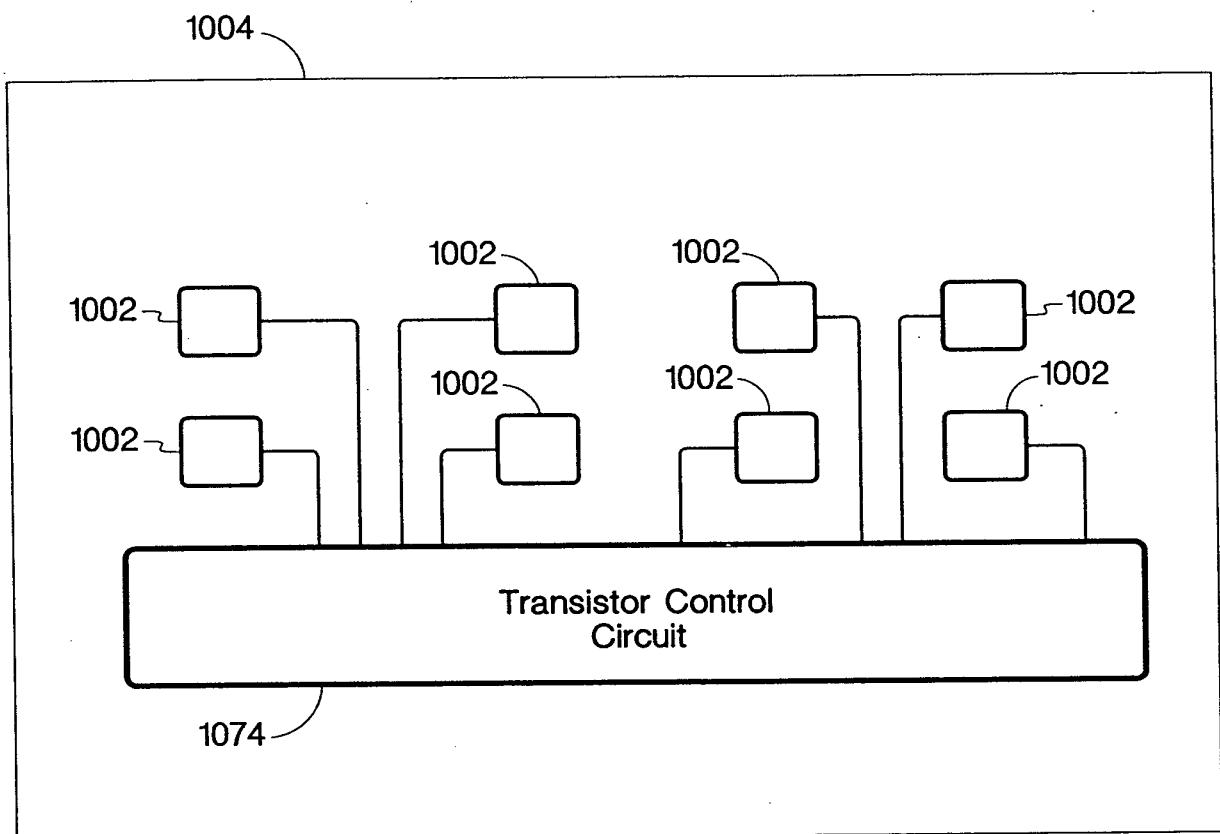


Fig. 10

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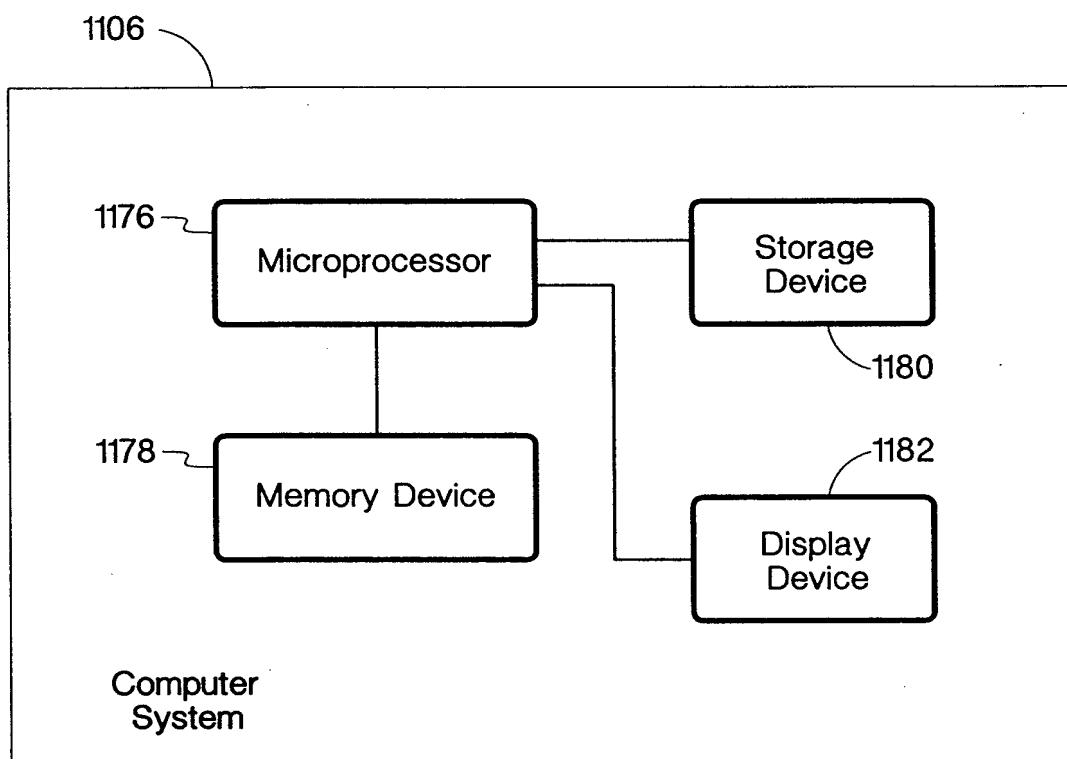


Fig. 11